

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = GULATI

First Name = MANU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09137583	6175911	150	08/21/1998	METHOD AND APPARATUS FOR CONCURRENTLY EXECUTING MULTIPLICATION AND ITERATIVE OPERATIONS	GULATI, MANU
09385186	Not Issued	161	08/30/1999	APPARATUS AND METHOD FOR MAINTAINING AN ARCHITECTURAL STATUS REGISTER	GULATI, MANU
10269666	6912602	150	10/11/2002	SYSTEM HAVING TWO OR MORE PACKET INTERFACES, A SWITCH, AND A SHARED PACKET DMA CIRCUIT	GULATI, MANU
10269922	7206879	150	10/11/2002	SYSTEMS USING MIX OF PACKET, COHERENT, AND NONCOHERENT TRAFFIC TO OPTIMIZE TRANSMISSION BETWEEN SYSTEMS	GULATI, MANU
10270016	7227870	150	10/11/2002	SYSTEMS INCLUDING PACKET INTERFACES, SWITCHES, AND PACKET DMA CIRCUITS FOR SPLITTING AND MERGING PACKET STREAMS	GULATI, MANU
10270029	6748479	150	10/11/2002	SYSTEM HAVING INTERFACES AND SWITCH THAT SEPARATES COHERENT AND PACKET TRAFFIC	GULATI, MANU
10356321	6944719	150	01/31/2003	SCALABLE CACHE COHERENT DISTRIBUTED SHARED MEMORY PROCESSING SYSTEM	GULATI, MANU
10356323	7403525	150	01/31/2003	EFFICIENT ROUTING OF PACKET DATA IN A SCALABLE PROCESSING RESOURCE	GULATI, MANU
10356324	7346078	150	01/31/2003	PROCESSING OF RECEIVED DATA WITHIN A MULTIPLE PROCESSOR DEVICE	GULATI, MANU
10356346	Not Issued	41	01/31/2003	Bandwidth allocation fairness within a processing system of a plurality of processing devices	GULATI, MANU

10356348	Not Issued	41	01/31/2003	Transmitting data from a plurality of virtual channels via a multiple processor device	GULATI, MANU
10356390	Not Issued	61	01/31/2003	Multiple processor integrated circuit having configurable packet-based interfaces	GULATI, MANU
10356661	Not Issued	41	01/31/2003	Packet data service over hyper transport link(s)	GULATI, MANU
10421988	Not Issued	41	04/23/2003	Smart routing between peers in a point-to-point link based system	GULATI, MANU
10439297	6941440	150	05/15/2003	ADDRESSING SCHEME SUPPORTING VARIABLE LOCAL ADDRESSING AND VARIABLE GLOBAL ADDRESSING	GULATI, MANU
10675745	Not Issued	61	09/30/2003	Management of received data within host device using linked lists	GULATI, MANU
10684915	7272151	150	10/14/2003	CENTRALIZED SWITCHING FABRIC SCHEDULER SUPPORTING SIMULTANEOUS UPDATES	GULATI, MANU
10684989	7313146	150	10/14/2003	TRANSPARENT DATA FORMAT WITHIN HOST DEVICE SUPPORTING DIFFERING TRANSACTION TYPES	GULATI, MANU
10684998	7319702	150	10/14/2003	APPARATUS AND METHOD TO RECEIVE AND DECODE INCOMING DATA AND TO HANDLE REPEATED SIMULTANEOUS SMALL FRAGMENTS	GULATI, MANU
10685231	Not Issued	71	10/14/2003	Apparatus and method to receive and align incoming data in a buffer to expand data width by utilizing a single write port memory device	GULATI, MANU
10742060	7490187	150	12/20/2003	HYPERTRANSPORT/SPI-4 INTERFACE SUPPORTING CONFIGURABLE DESKEWING	GULATI, MANU
10861624	6941406	150	06/04/2004	SYSTEM HAVING INTERFACES AND SWITCH THAT SEPARATES COHERENT AND PACKET TRAFFIC	GULATI, MANU
11069313	Not Issued	161	03/01/2005	System having two or more packet interfaces, a switch, and a shared packet DMA circuit	GULATI, MANU
11146449	Not Issued	161	06/07/2005	System having interfaces and switch that separates coherent and packet traffic	GULATI, MANU
11146450	Not Issued	161	06/07/2005	Addressing scheme supporting variable local addressing and	GULATI, MANU

				variable global addressing	
<a href="#"><u>11182123</u></a>	<a href="#"><u>7171521</u></a>	150	07/15/2005	COHERENT SHARED MEMORY PROCESSING SYSTEM	GULATI, MANU
<a href="#"><u>11717511</u></a>	<a href="#"><u>7424561</u></a>	150	03/13/2007	SYSTEMS USING MIX OF PACKET, COHERENT, AND NONCOHERENT TRAFFIC TO OPTIMIZE TRANSMISSION BETWEEN SYSTEMS	GULATI, MANU
<a href="#"><u>11786275</u></a>	Not Issued	51	04/11/2007	Receiving data from virtual channels	GULATI, MANU
<a href="#"><u>11803637</u></a>	Not Issued	30	05/15/2007	Systems including packet interfaces, switches, and packet DMA circuits for splitting and merging packet streams	GULATI, MANU
<a href="#"><u>12362679</u></a>	Not Issued	25	01/30/2009	Hypertransport/SPI-4 Interface Supporting Configurable Deskewing	GULATI, MANU
<a href="#"><u>60331789</u></a>	Not Issued	159	11/20/2001	Packet data service over hyper transport link(s)	GULATI, MANU
<a href="#"><u>60344713</u></a>	Not Issued	159	12/24/2001	Multi-function hypertransport devices	GULATI, MANU
<a href="#"><u>60348717</u></a>	Not Issued	159	01/14/2002	Hyper transport coupled distributed system host	GULATI, MANU
<a href="#"><u>60348777</u></a>	Not Issued	159	01/14/2002	Multi-function hypertransport devices	GULATI, MANU
<a href="#"><u>60380740</u></a>	Not Issued	159	05/15/2002	System on chip for networking	GULATI, MANU
<a href="#"><u>60419031</u></a>	Not Issued	160	10/16/2002	Processing of received data within a multiple processor device	GULATI, MANU
<a href="#"><u>60419032</u></a>	Not Issued	159	10/16/2002	Multiple processor integrated circuit having configurable packet-based interfaces	GULATI, MANU
<a href="#"><u>60419033</u></a>	Not Issued	159	10/16/2002	Scalable cache coherent distributed shared memory processing system	GULATI, MANU
<a href="#"><u>60419040</u></a>	Not Issued	159	10/16/2002	Transmitting data from a plurality of virtual channels via a multiple processor device	GULATI, MANU
<a href="#"><u>60419041</u></a>	Not Issued	159	10/16/2002	Packet data service over hypertransport links	GULATI, MANU
<a href="#"><u>60419042</u></a>	Not Issued	159	10/16/2002	Efficient routing of packet data in a scalable processing resource	GULATI, MANU
<a href="#"><u>60419043</u></a>	Not Issued	159	10/16/2002	Bandwidth allocation fairness within a processing system of a plurality of processing devices	GULATI, MANU
<a href="#"><u>60520166</u></a>	Not Issued	159	11/14/2003	Hyper transport/SPI-4 interface supporting configurable deskewing	GULATI, MANU

Inventor Search Completed: No Records to Display.

**Search Another: Inventor****Last Name**

GULATI

**First Name**

MANU

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